

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Mitsuhiro Ishiguro	<b>Examiner:</b>	Unassigned
<b>Serial No:</b>	Unassigned	<b>Art Unit:</b>	Unassigned
<b>Filed:</b>	Herewith	<b>Docket:</b>	17228
<b>For:</b>	SEMICONDUCTOR MEMORY	<b>Dated:</b>	November 12, 2003


Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CLAIM OF PRIORITY**

Sir:

Applicant in the above-identified application hereby claims the right of priority in connection with Title 35 U.S.C. § 119 and in support thereof, will submit in due course a certified copy of Japanese Patent Application No. 2002-327239, filed on November 11, 2002.

Respectfully submitted,

  
Paul J. Esatto, Jr.  
Registration No.: 30,749

Scully, Scott, Murphy & Presser  
400 Garden City Plaza  
Garden City, New York 11530  
(516) 742-4343

---

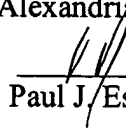
**CERTIFICATE OF MAILING BY "EXPRESS MAIL"**

Express Mailing Label No.: EV247990524US

Date of Deposit: November 12, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Dated November 12, 2003

  
Paul J. Esatto, Jr.